



SHEET 1 OF 1

<b>INFORMATION DISCLOSURE CITATION</b> PTO-1449		<b>CUSTOMER NUMBER</b> 44987	ATTORNEY'S Dkt No. 0050-0012	APPLICATION NO. 09/977,227		
			APPLICANT(S) Ruixie FAN et al.			
		FILING DATE October 16, 2001	GROUP 2661			
<b>U.S. PATENT DOCUMENTS</b>						
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
BAP	6,831,922 B1	12/14/2004	Shimada	370	403	
↓	6,618,379 B1	09/09/2003	Ramamurthy et al.	370	395.41	
↓	2001/0033581A1	10/25/2001	Kawarai et al.	370	468	
<b>FOREIGN PATENT DOCUMENTS</b>						
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation
						Yes
BAP	11-239151	8/31/1999	Japan			X (Abstract Only)
↓	10-229404	8/25/1998	Japan			X (Abstract Only)
↓	2000-349828	12/15/2000	Japan			X (Abstract Only)
↓	2000-349765	12/15/2000	Japan			X (Abstract Only)
<b>OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)</b>						
BAP	Naoki Matsuoka et al.; "A Study on Terabit Switch Architecture with QoS Control", Technical Report of IEICE, SSE2000-40, Vol. 100, No. 79; May, 2000; 8 pages. (Abstract in English only)					
EXAMINER <i>Bs A Pm</i>	DATE CONSIDERED <i>1/4/06</i>					

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